

SPECIFICATION FOR White LED

LML-LK-WZ-0.5W

- Serial Number: LML-LK-WZ-0.5W-RunNo-BinNo-C1.6T-CRI-CCT
- 1W maximum power capability
- Lead-free reflow soldering application
- Built-in ESD protection device
- RoHS compliant

Ver	Vendor					
Written	Written Approval					

Lumens CO., LTD.

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1. Product description

* Description

- The LK series LED is designed for the high power operation to get the high flux output applications.
- It incorporates the state of the art SMD design and high reliable material.
- It is ideal for the light source for general illumination applications, custom designed solutions.

* Features

- Maximum drive current up to 300mA
- Low thermal resistance as low as 8 ℃/W
- Wide viewing angle of 120~140 degrees
- Reflow soldering with JEDEC JSTD-020C compatible
- RoHS compliant

* Applications

- General luminaire
- Bulb
- Downlight

2. Absolute maximum ratings

Parameters	Symbol	Min Value	Max Value	Unit
Power dissipated	Pd		0.94	w
Rated forward current	If		300	mA
Allowable peak forward current(1)	Ip		350	mA
Maximum junction temperature capability	Тј		125	°
Electrostatic discharge threshold(2)	ESD		±5,000	V
Operating temperature	Topr	-40	+85	°
Storage temperature	Tstg	-40	+85	°
Soldering temperature(Reflow)	Tsol		260℃, 10s	℃, s
Soldering temperature(Hand)	Tsoh		350℃, 3s	℃, s

⁽¹⁾ Ip measured at 1/10 duty cycle, 0.1ms pulse width.

3. Electro-optical characteristics (Ta=25°C, If=130mA)

Parameters	Symbol	Condition	Min.	Тур.	Max.	Unit
Luminous Flux	Фv	If=130mA	40		60	lm
Correlated Color Temperature	ССТ	If=130mA	2,700		8,000	К
Color Rendering Index	CRI	If=130mA		80		-
Forward voltage	Vf	If=130mA	2.8		3.1	V
Viewing angle FWHM	201/2	If=130mA		130		deg
Thermal resistance junction to solder pad	Rthj-s			12		°C/W
Reverse voltage	Vr	If=10uA		0.6		V
Temperature coefficient Vf	TCv	If=130mA		-3		mV/°C

⁽¹⁾ Parameters are measured by CAS-140 of Instrument System CO.,LTD.

⁽²⁾ ESD HBM class 2 per Mil-Std-883D method 3015.

⁽²⁾ Measurement accuracy : $\Phi v(\pm 10\%)$, Vf($\pm 0.05V$).



4. Electro-optical chart

ССТ(К)	If(mA)	Vf(V)	Pd(W)	Φv(lm)	lm/W
	65	2.78	0.18	25.45	140.97
2700	120	2.88	0.35	44.84	129.71
2700	130	2.89	0.38	48.22	128.22
	300	3.14	0.94	100.34	106.54
	65	2.78	0.18	26.09	144.54
2000	120	2.88	0.35	46.08	133.30
3000	130	2.89	0.38	48.82	129.81
	300	3.14	0.94	104.83	111.32
	65	2.78	0.18	28.20	156.19
4000	120	2.88	0.35	49.68	143.71
4000	130	2.89	0.38	53.42	142.06
	300	3.14	0.94	111.17	118.05
	65	2.78	0.18	28.92	160.20
5000	120	2.88	0.35	50.95	147.39
3000	130	2.89	0.38	54.79	145.70
	300	3.14	0.94	114.02	121.07
	65	2.78	0.18	28.48	157.79
5700	120	2.88	0.35	50.19	145.18
3700	130	2.89	0.38	53.97	143.52
	300	3.14	0.94	112.31	119.26
	65	2.78	0.18	27.91	154.59
6500	120	2.88	0.35	49.17	142.24
6500	130	2.89	0.38	52.88	140.60
	300	3.14	0.94	110.03	116.84

Parameters are measured by CAS-140 of Instrument System CO.,LTD.
Measurement accuracy: Φν(±10%), Vf(±0.05V).

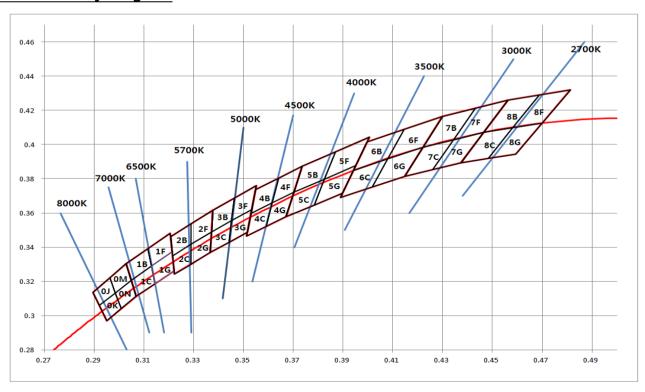


5. Ranks

Item	Symbol	CCT(K)	Rank	Min.	Тур.	Max.	Unit	Condition
			Α	40	-	45		
		2,700	В	45	-	50	lm	
			C	50	-	55		
			Α	40	-	45		
		3,000	В	45	-	50	lm	
			С	50	-	55		
			Α	45	-	50		
		4,000	В	50	-	55	lm	130mA
Luminous Flux	Фν		С	55	-	60		
Luminous Flux		5,000	Α	45	-	50	lm	
			В	50	-	55		
			С	55	-	60		
			Α	45	-	50		
		5,700	В	50	-	55	lm	
			С	55	-	60		
			Α	45	-	50		
		6,500	В	50	-	55	lm	
			C	55	=	60		
			1	2.8	-	2.9		
Forward Voltage	Vf	I - N	2	2.9	-	3	V	
			3	3	-	3.1		

- (1) Parameters are measured by CAS-140 of Instrument System CO.,LTD.
- (2) Measurement accuracy : $\Phi \mathbf{v}(\pm 10\%)$, Vf(± 0.05 V).

6. Chromaticity diagram



(1) Chromaticity coordinate groups are measured with an accuracy of $\pm 0.01\,$



7. Correlated Color Temperature Ranges

Item	CCT Ranges	CIE Ranges	Color bins
Cool-White	8000K ~ 4750K	0J ~ 3G	16bin
Neutral-White	4750K ~ 3750K	4B ~ 5G	8bin
Warm-White	3750K ~ 2600K	6B ~ 8G	12bin

8. Chromaticity coordinates

1) Cool

ССТ	Rank	CIE X	CIE Y	CCT	Rank	CIE X	CIE Y	CCT	Rank	CIE X	CIE Y	ССТ	Rank	CIE X	CIE Y			
		0.2920	0.3060			0.3028	0.3304			0.3207	0.3462			0.3376	0.3616			
	OJ	0.2895	0.3135		1B	0.3115	0.3391		2B	0.3290	0.3538		3B	0.3463	0.3687			
	0,	0.2962	0.3220		10	0.3130	0.3290			0.3290	0.3417			0.3451	0.3554			
		0.2984	0.3133			0.3048	0.3207			0.3215	0.3350			0.3371	0.3490			
		0.2950	0.2970						0.3048	0.3207			0.3215	0.3350			0.3371	0.3490
	0K	0.2920	0.3060		1C	0.3130	0.3290		2C	0.3290	0.3417		3C	0.3451	0.3554			
	UK	0.2984 0.3133		IC.	0.3144	0.3186		20	0.3290	0.3300		30	0.3440	0.3427				
7500K		0.3009	0.3042	6500K		0.3068	0.3113	5700K		0.3222	0.3243	5000K		0.3366	0.3369			
/300K		0.2984	0.3133	6300K	1F	0.3115	0.3391		2F	0.3290	0.3538	30000	3F	0.3463	0.3687			
	0M	0.2962	0.3220			0.3205	0.3481			0.3376	0.3616			0.3551	0.3760			
	UIVI	0.3028	0.3304		TL	0.3213	0.3373		2F	0.3371	0.3490		35	0.3533	0.3620			
		0.3048	0.3207			0.3130	0.3290			0.3290	0.3417			0.3451	0.3554			
		0.2984	0.3133			0.3130	0.3290			0.3290	0.3417			0.3451	0.3554			
	0N	0.3048	0.3207		1G	0.3213	0.3373		2G	0.3371	0.3490		3G	0.3533	0.3620			
		0.3068	0.3113			0.3221	0.3261		2G	0.3366	0.3369		36	0.3515	0.3487			
		0.3009	0.3009 0.3042 0.3144 0.3186	0	0.3290	0.3300			0.3440	0.3427								

2) Netural

ССТ	Rank	CIE X	CIE Y	ССТ	Rank	CIE X	CIE Y	ССТ	Rank	CIE X	CIE Y	ССТ	Rank	CIE X	CIE Y	
		0.3548	0.3736			0.3736	0.3874									
	4B	0.3641	0.3804		5B	0.3870	0.3958									
	40	0.3615	0.3659		36	0.3825	0.3798									
		0.3530	0.3597			0.3702	0.3722									
		0.3530	0.3597			0.3702	0.3722									
	4C	0.3615	0.3659		5C	0.3825	0.3798									
	40	0.3590	0.3521		30	0.3783	0.3646									
4500K		0.3512	0.3465	4000K		0.3670	0.3578									
4300K		0.3641	0.3804	4000K		0.3870	0.3958									
	4F	0.3736	0.3874		5F	0.4006	0.4044									
		0.3702	0.3722		35	0.3951	0.3876									
		0.3615	0.3659			0.3825	0.3798									
		0.3615	0.3659			0.3825	0.3798									
	4G	0.3702	0.3722		5G	0.3951	0.3876]								
	-0	0.3670	0.3578		36	0.3898	0.3716]								
		0.3590	0.3521			0.3783	0.3646									

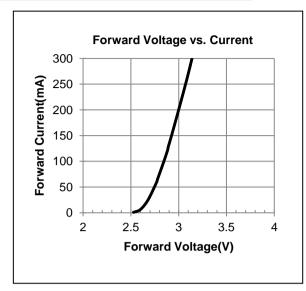
3) Warm

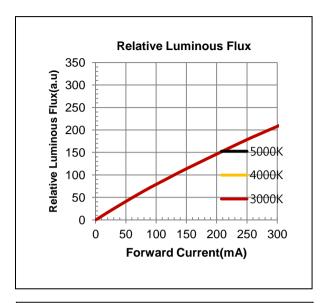
CCT	Rank	CIE X	CIE Y	CCT	Rank	CIE X	CIE Y	CCT	Rank	CIE X	CIE Y	CCT	Rank	CIE X	CIE Y
		0.3996	0.4015			0.4299	0.4165			0.4562	0.4260				
	6B	0.4146	0.4089		7B	0.4430	0.4212		8B	0.4687	0.4289				
	00	0.4080	0.3916		0.4342	0.4028		0.0	0.4582	0.4099					
		0.3941	0.3848			0.4221 0.3985	j		0.4465	0.4071	1				
		0.3941	0.3848			0.4221	0.3985			0.4465	0.4071				
	6C	0.4080	0.3916		7C	0.4342 0.4028		8C	0.4582	0.4099	7				
	BC	0.4017	0.3752		0.4260 0.3853		٥٥	0.4483	0.3918						
3500K		0.3889	0.3690	3000K		0.4147	0.3814	27001/		0.4373	0.3893	1			
33000		0.4146	0.4089	30000		0.4430	0.4212	2700K		0.4687	0.4289	1			
	6F	0.4299	0.4165		7F	0.4562	0.4260		8F	0.4813	0.4319				
	OF .	0.4221	0.3985		/F	0.4465	0.4071		or	0.4700	0.4126				
		0.4080	0.3916			0.4342	0.4028			0.4582	0.4099	┐			
		0.4080	0.3916			0.4342	0.4028		0.4582 0.4099	0.4099	`				
	6G	0.4221	0.3985		7G	0.4465	0.4071	8G	0.0	0.4700	0.4126				
	86	0.4147	0.3814		/6	0.4373	0.3893		— I 8G 1—	0.4593	0.3944	1			
		0.4017	0.3752			0.4260	0.3853]		0.4483	0.3918	1			/

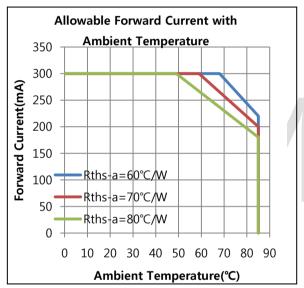
(1) Chromaticity coordinate groups are measured with an accuracy of $\pm 0.01\,$

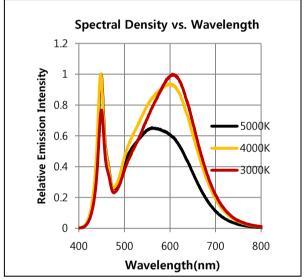


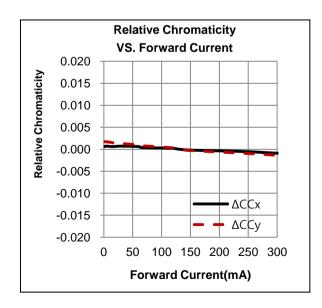
9. Characteristic Graphs(Ta=25°C)

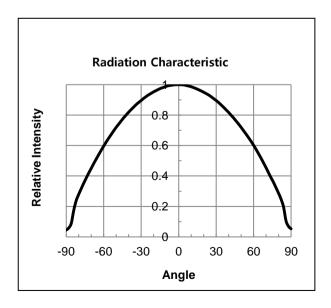










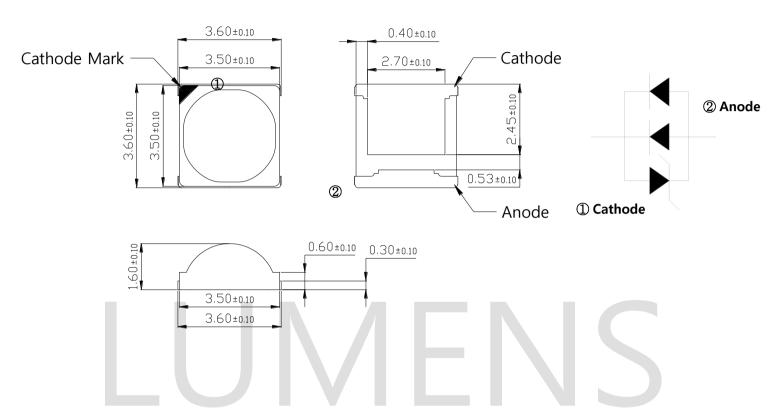




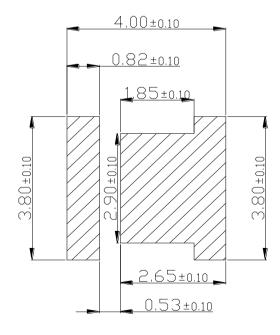
10. Outline Dimensions

Unit: mm

- 1) Package outline: (LxWxH) of 3.5 x 3.5 x 1.6 mm.
- 2) Tolerance All measurements are \pm 0.15 mm unless otherwise indicated.



11. Recommended solder pad



(Unit: mm)



12. Reliability test items and conditions

Item	Reference	Test Conditions	Duration / Cycle	Number of Damaged
Thermal Shock	EIAJ ED-4701	Ta =-40°C (30min) ~ 100°C (30min)	150 Cycle	0/30
Operating Endurance Test	Internal Reference	Ta =25°C, IF =300mA	1,000 Hours	0/30
High Temperature High Humidity Life Test	Internal Reference	Ta =60°C, RH=90%, IF =250mA	500 Hours	0/30
High Temperature Life Test	Internal Reference	Ta =85℃, IF =150mA	500 Hours	0/30
ESD(HBM)		±5KV at 1.5kΩ, 100pF	5 Time	0/5
Reflow	Tsor	260°C< 10sec, Reflow Soldering	3 Time	0/30

⁻ Test Board : Metal board thickness=1.6mm, Copper layer thickness=0.07mm, Rth ± 80 °C/W

◆ CRITERIA FOR JUDGING THE DAMAGE

Thomas	Combal	Condition	Criteria for Judgment			
Item	Symbol	Condition	MIN	MAX		
Forward Voltage	Vf	IF =130mA	7- 6	USL (1) × 1.1		
Radiant Power	Ро	IF =130mA	LSL (2) × 0.7	-		

⁽¹⁾ USL: Upper Standard Level

⁽²⁾ LSL: Lower Standard Level



13. Recommended soldering temperature - time profile for reflow soldering

Surface Mounting Condition

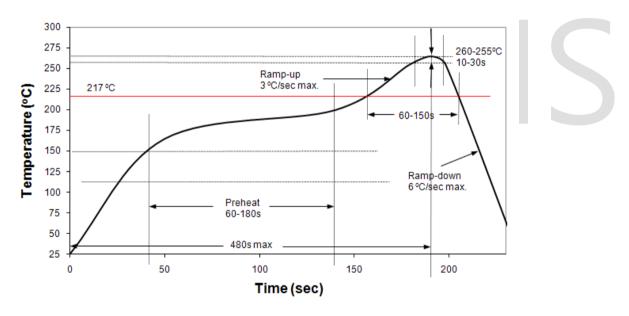
In automatic mounting of the SMD LEDs on printed circuit boards, any bending, expanding and pulling forces or shock against the SMD LEDs should be kept minimum to prevent them from electrical failures and mechanical damages of the devices.

Soldering Reflow

- -Soldering of the SMD LEDs should conform to the soldering condition in the individual specifications.
- -SMD LEDs are designed for reflow soldering.
- -In the reflow soldering, too high temperature and too large temperature gradient such as rapid heating/cooling may cause electrical & optical failures and damages of the devices.
- -Lumens cannot guarantee the LEDs after they have been assembled using the solder dipping method.

Recommended Pb Free IR-Reflow Soldering Profile.

Classification Reflow Profile (JEDEC J-STD-020C)

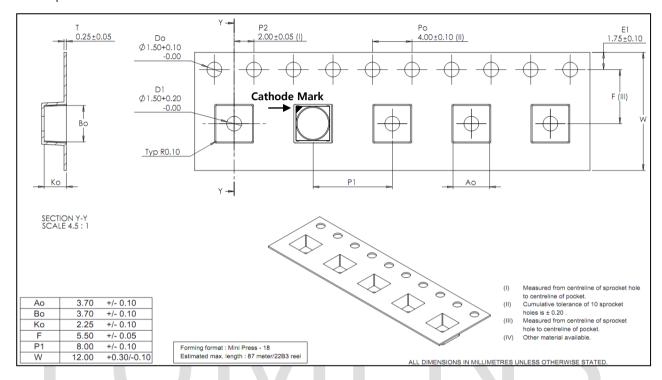




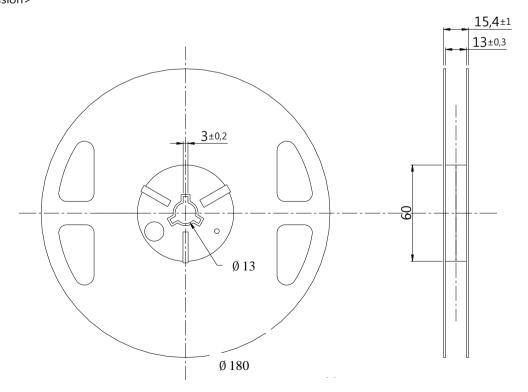
14. Taping and orientation

- 1. Moisture proof bag.
- 2.1 Reel/bag.
- 3. Quantity: 800ea/Reel.

<Carrier tape Dimension>



<Reel Dimension>





15. Cautions

- 1 Moisture-Proof Package
 - 1.1 When moisture is absorbed into the LED package it may vaporize and expand products during soldering. There is a possibility that this may cause exfoliation of the contacts and damage to the optical characteristics of the LEDs. For this reason, the moisture-proof package is used to keep moisture to a minimum in the package.
 - 1.2 A package of a moisture-absorbent material (silica gel) is inserted into the shielding bag. The silica gel changes its color from blue to pink as it absorbs moisture.

2 Current limiting

A resistor should be used to limit current spikes that can be caused by voltage fluctuations. Otherwise damage could occur.

3 Iron Soldering

- 3.1 Hand soldering is not recommended for regular production. These guidelines are for rework only.
- 3.2 Soldering iron tip should contact each terminal no more than 3 sec at 120°C, using soldering iron with nominal power less than 25W. Allow min. 2 sec. between soldering intervals.

4 Storage Conditions

- 4.1 Before opening the package: The LEDs should be kept at 30°C or less and 90%RH or less. The LEDs should be used within a year. When storing the LEDs, moisture-proof packaging with moisture-absorbent material (silica gel) is recommended.
- 4.2 After opening the package: The LEDs should be kept at 30℃ or less and 70%RH or less. The LEDs should be soldered within 168 hours (7 days) after opening the package. If unused LEDs remain, they should be stored in moisture-proof packages, such as sealed containers with packages of moisture-absorbent material (silica gel). It is also recommended to return the LEDs to the original moisture-proof bag and to reseal the moisture-proof bag again.
- 4.3 If the moisture-absorbent material (silica gel) has faded away or the LEDs have exceeded the recommended storage time, baking treatment should be performed using the following conditions. Baking treatment: more than 24 hours at 65±5℃
- 4.4 Lumens LED electrode sections are comprised of a silver-plated copper alloy. The silver surface may be affected by environments which contain corrosive gases and so on. Please avoid condition which may cause difficulty environments during soldering operations. It is recommended that the user uses the LEDs as soon as possible.
- 4.5 Please avoid rapid transitions in ambient temperature, especially in high humidity environments where condensation can occur.
- 5 Handling of Silicone Lens LEDs
 - 5.1 Avoid silicone resin parts especially with sharp tools such as pincette(tweezers).
 - 5.2 Avoid leaving fingerprints on silicone lens part.
 - 5.3 Do not apply the silicone lens part with pressure especially in SMT production. So use a proper nozzle not to press the lens part of the LED to pick and place.

6 Usage

6.1 Do not exceed the values given in this specification.

NOTE:

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